



## COURSE DESCRIPTION CARD - SYLLABUS

Course name

Electronics and power electronics [S1Eltech1>EiE1]

### Course

Field of study

Electrical Engineering

Year/Semester

2/3

Area of study (specialization)

–

Profile of study

general academic

Level of study

first-cycle

Course offered in

polish

Form of study

full-time

Requirements

compulsory

### Number of hours

Lecture

30

Laboratory classes

0

Other (e.g. online)

0

Tutorials

0

Projects/seminars

0

### Number of credit points

3,00

### Coordinators

dr hab. inż. Michał Gwóźdź prof. PP  
michal.gwozdz@put.poznan.pl

### Lecturers

### Prerequisites

Knowledge in the field of mathematics, physics and circuit theory at the level of the first year of study. Ability to understand and interpret the transmitted messages and effective self-education in the field related to the chosen field of study.

### Course objective

Familiarization with the construction, parameters and applications of basic electronic components. Getting to know the principles of operation of analog and digital electronic circuits. Acquiring the ability to design electronic circuits at the basic level.

### Course-related learning outcomes

Knowledge:

1. Knows and understands the basic laws of electrical engineering, properties of elements of electrical circuits, has detailed knowledge of the theory of electrical circuits (for steady and transient states), knows and understands the theory of long line [K1\_W04].
2. Has basic knowledge about the life cycle of microprocessor devices, systems and systems and their applications in selected industries [K1\_W07].

3. Knows the structure and operation of electronic, optoelectronic and simple analog and digital electronic and power electronic devices, understands the processes occurring in their life cycle [K1\_W014].

Skills:

1. Is able to use a foreign language at B2 level of the European System of the Description of Language Education, as well as read and understand catalog cards, application notes, standards and technical documentation as well as manuals for electrical equipment [K1\_U01].

2. Is able to design and manufacture, in accordance with the given specification and using appropriate methods, techniques, tools and materials, typical electrical systems intended for various applications [K1\_U03].

Social competences:

Is aware of the need to initiate actions for the public interest, understands the various aspects and effects of electrical engineer activities, including environmental impact, and the associated responsibility for decisions [K1\_K02].

### Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

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Assessment of knowledge and skills demonstrated during the written test-problem exam - based on the number of points obtained.

### Programme content

Properties and characteristics of basic electronic components and devices: passive components, semiconductor diodes, bipolar and field effect transistors and their operating systems and applications. Semiconductor optoelectronic devices - properties and examples of applications. Feedback in analog circuits. The issue of closed system stability. Operational amplifiers - ideal and real and their: properties, parameters and applications. Power amplifiers: distribution, properties and applications. Electronic generators: conditions of vibration generation, types and applications of generators. Linear systems - basic properties. Analog filters - types, parameters and design principles. AC adapter - structure and parameters. Fundamentals of digital technology: binary number recording system, logical states and logical operations, logical functors, truth table, digital combination and sequential blocks. Basic applications of digital circuits. Basic chip systems of TTL and CMOS families - construction and parameters. Semiconductor memories - general classification and basic parameters of selected types of memory.

### Teaching methods

Lecture with multimedia presentation (diagrams, formulas, definitions, etc.) supplemented by the content given on the board.

### Bibliography

Basic

1. Z. Kulka, M. Nadachowski, Analogowe układy scalone, WKŁ, W-wa, 1980.
2. J. Kalisz, Podstawy elektroniki cyfrowej, WKiŁ, W-wa, 2002.
3. P. Górecki, Wzmacniacze operacyjne, Wydawnictwo BTC, W-wa, 2004.
4. P. Horowitz, W. Hill, Sztuka elektroniki. Część 1 i 2, WKŁ, 2014.

Additional

1. M.P. Kaźmierkowski, J.T. Matysik, Wprowadzenie do elektroniki i energoelektroniki, Oficyna Wyd. PW, W-wa, 2005.
2. P. Scherz, S. Monk, Practical Electronics for Inventors, Fourth Edition, Mc Graw Hill, 2016.

### Breakdown of average student's workload

	Hours	ECTS
Total workload	57	3,00
Classes requiring direct contact with the teacher	32	2,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	25	1,00